Frequency Response of Amplifiers

Association of Poles with Nodes





Example



Miller Effect







- Strictly speaking, the gain must be calculated at the frequency of interest.
- Not every circuit lends itself to Miller decomposition.
- Example

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Does the circuit have two poles?!

Common- Source Stage

(Also half circuit of a differential pair) Use of Miller's Theorem:



If R_s is relatively large,

$$Z_X = \frac{1}{C_{\epsilon q} s} || \left(\frac{C_{GD}}{C_{GD} + C_{GS}} \cdot \frac{1}{g_{m1}} \right)$$

$$\omega_{in} = \frac{1}{R_S [C_{GS} + (1 + g_m R_D) C_{GD}]}.$$
$$\omega_{out} = \frac{1}{R_D (C_{DB} + C_{GD})}$$



$$\omega_{out} = \frac{1}{[R_D||\left(\frac{C_{GD}}{C_{GD} + C_{GS}} \cdot \frac{1}{g_{m1}}\right)](C_{\epsilon q} + C_{DB})}$$

Exact Analysis:



$$\frac{V_{out}}{V_{in}(s)} = \frac{(C_{GD}s - g_m)R_D}{R_S R_D \xi s^2 + [R_S(1 + g_m R_D)C_{GD} + R_S C_{GS} + R_D(C_{GD} + C_{DB})]s + 1}$$

$$\xi = C_{GS}C_{GD} + C_{GS}C_{DB} + C_{GD}C_{DB}$$

The circuit has one zero and two poles even though there are three caps.

Dominant Pole Approximation:

$$\omega_{p1} = \frac{1}{R_S(1 + g_m R_D)C_{GD} + R_S C_{GS} + R_D(C_{GD} + C_{DB})}$$

Example



A few notes on CS response:

1. Second pole can also be found using the dominant pole appr.:

$$\begin{split} \omega_{p2} &= \frac{1}{\omega_{p1}} \cdot \frac{1}{R_S R_D (C_{GS} C_{GD} + C_{GS} C_{DB} + C_{GD} C_{DB})} \\ &= \frac{R_S (1 + g_m R_D) C_{GD} + R_S C_{GS} + R_D (C_{GD} + C_{DB})}{R_S R_D (C_{GS} C_{GD} + C_{GS} C_{DB} + C_{GD} C_{DB})} \end{split}$$

If C_{GS} is large enough,



The zero can also be calculated by noting that



3. The input impedance can be estimated using Miller's Theorem:



Or more precisely:

$$\frac{V_X}{I_X} = \frac{1 + R_D (C_{GD} + C_{DB})s}{C_{GD} s (1 + g_m R_D + R_D C_{DB} s)}$$



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If the frequency is low enough such that:

 $|R_D(C_{GD} + C_{DB})s| \ll 1 \text{ and } |R_DC_{DB}s| \ll 1 + g_m R_D$

then Miller multiplication works. What happens if C_{GD} is very large?

Source Follower



$$\frac{V_{out}}{V_{in}}(s) = \frac{g_m + C_{GS}s}{R_S(C_{GS}C_L + C_{GS}C_{GD} + C_{GD}C_L)s^2 + (g_m R_S C_{GD} + C_L + C_{GS})s + g_m}$$

The zero is in the left-half plane (why?). Can use dominant pole appr. to estimate the poles (but in practice does not happen often):

With Rs = 0,

 $\omega_{p1} \approx \frac{g_m}{g_m R_S C_{GD} + C_L + C_{GS}}$ $= \frac{1}{R_S C_{GD} + \frac{C_L + C_{GS}}{g_m}}.$

- Input Impedance $V_X = \frac{I_X}{C_{GS}s} + \left(\frac{I_X}{C_{GS}s} + \frac{g_m I_X}{C_{GS}s}\right) \left(\frac{1}{g_{mb}} || \frac{1}{C_L s}\right) \quad v_X \bigoplus_{=}^{I_X} \bigoplus_{=}^{I_X} \bigcup_{=}^{I_X} V_{out}$ $Z_{in} = \frac{1}{C_{GS}s} + \left(1 + \frac{g_m}{C_{GS}s}\right) \frac{1}{g_{mb} + C_L s}$

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Can we represent Z_{out} with a passive RL network? Since Z_{out} is of first order, it can have only one inductor and no other storage element. Considering the cases at f = o and f = ∞ , we arrive at this equivalent circuit:



The inductive behavior resulting from finite source impedance may cause significant ringing or even oscillation in the presence of heavy load capacitance. $- v_{DD}^{V}$



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Fall 14 <u>Common-Gate Stage</u> (No Miller effect) V_{DD} C_D R_D V_{out} R_S M_1 V_{in} V_{in} V_{DD} V_{out}

$$\frac{V_{out}}{V_{in}}(s) = \frac{(g_m + g_{mb})R_D}{1 + (g_m + g_{mb})R_S} \frac{1}{(1 + \frac{C_S}{g_m + g_{mb} + R_S^{-1}}s)(1 + R_D C_D s)}$$

Input impedance in the presence of channel-length modulation:

$$Z_{in} pprox rac{Z_L}{(g_m + g_{mb})r_O} + rac{1}{g_m + g_{mb}}$$

Example:



$$(-V_{out}C_Ls + V_1C_{in}s)R_S + V_{in} = -V_1 \qquad V_1 = -\frac{-V_{out}C_LsR_S + V_{in}}{1 + C_{in}R_Ss}$$

$$\frac{V_{out}}{V_{in}}(s) = \frac{1 + g_m r_O}{r_O C_L C_{in} R_S s^2 + [r_O C_L + C_{in} R_S + (1 + g_m r_O) C_L R_S] s + 1}$$
$$Z_{in} = \frac{1}{g_m + g_{mb}} + \frac{1}{C_L s} \cdot \frac{1}{(g_m + g_{mb}) r_O}.$$

Why does Zin become independent of C_L at high frequencies?

Cascode Stage Miller effect much less significant here. Why?

$$\omega_{p,A} = \frac{1}{R_S [C_{GS1} + (1 + \frac{g_{m1}}{g_{m2} + g_{mb2}})C_{GD1}]}$$

Poles at X and Y:

Example:





Differential Pair









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Comparison of fully-differential pair and circuit with active load:

Two poles:

- Miller effect at input
- Miller effect at input
- Load cap at output

Three poles:

- Miller effect at Vin2
- Load cap at Vout
- Time constant at X