#### **EE215C**

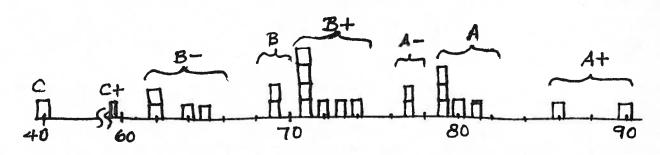
## Final Exam Winter 2009

Name: Solutions

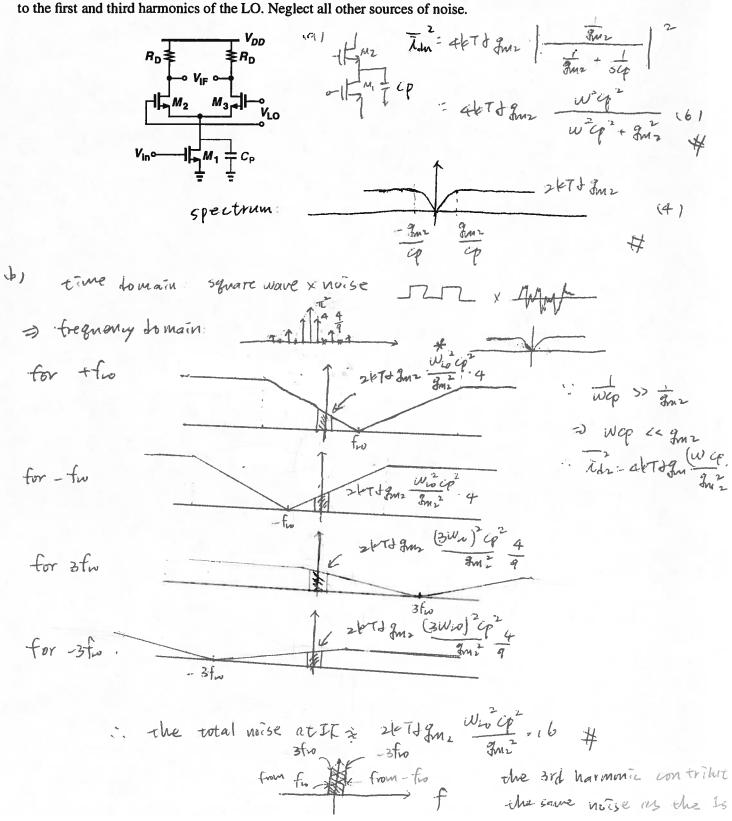
# Time Limit: 3 Hours Open Book, Open Notes

- 1. 20
- 2. 10
- 3. 10
- 4. 10
- 5. 15

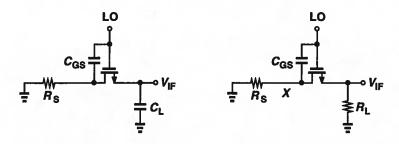
Total: 65



- 1. Consider the active mixer shown below, where all other capacitances are neglected and no transistor enters the triode region. Also, channel-length modulation and body effect are negligible.
- (a) Suppose  $M_2$  is on and  $M_3$  is off. Derive an expression for the small-signal drain current of  $M_2$  due to the thermal noise of  $M_2$ . Plot the spectrum of the drain current. Neglect all other sources of noise.
- (b) Now suppose the impedance of  $C_P$  is much larger than  $1/g_{m2}$  at the frequencies of interest, and the LO is applied with 50% duty cycle. If  $M_2$  and  $M_3$  switch abruptly, and only the first and third harmonics of the LO are considered, explain in detail how the thermal noise of  $M_2$  appears at IF. Sketch the IF noise spectrum due to the first and third harmonics of the LO. Neglect all other sources of noise.

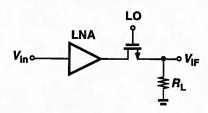


- 2. A student considers the arrangement shown on the left, where  $C_{GS}$  introduces LO leakge at the input and hence a dc component at the output. (Note that the capacitor does not carry a dc current.) The student then decides that the arrangement on the right is *free* from dc offsets, reasoning that a dc voltage,  $V_{ds}$ , at the output would require a dc current,  $V_{dc}/R_L$ , through  $R_L$  and hence an equal current through  $R_S$ . But this is impossible because a dc current through  $R_S$  gives rise to a negative voltage at node X.
- **5**
- (a) Explain in detail whether the student's conjecture is correct and why.
- (b) Neglecting  $C_{GS}$ , calculate the voltage conversion gain of the circuit on the right. Assume a square-wave LO with 50% duty cycle. Also, assume the switch has an on-resistanc of  $R_{on}$ .



- (a) The flaw in the student's reasoning is that a dc current at the output requires a dc current at the input. In other words, KCL only holds for all of the frequency components summed together not for each individual component. Thus, both circuits suffer from dc offsets.
- (b) The mixer operates as an ideal return-to-zero topology, except that Ron and RL attenuate the signal when the switch is on. That is, the input signal is multiplied by a square wave toggling between 0 and  $\frac{RL}{RL+Ron+Rs}$ . The conversion gain is therefore equal to  $\frac{RL}{RL+Ron+Rs}$ .

3. Shown below is part of a direct-conversion receiver. Suppose the LNA input-output characteristic can be expressed as  $y(t) = \alpha_1 x(t) + \alpha_2 x^2(t)$ . If the mixer switches abruptly with a 50% duty cycle and if  $R_L$  is much greater than the on-resistance of the switch and the output resistance of the LNA, compute the IP<sub>2</sub> of the receiver.



The LO signal can be represented as 
$$\frac{1}{2} + \sum_{n=1}^{\infty} \frac{2}{n\pi} \sin(\frac{n\pi}{2}) \cos(n\omega t)$$

for a two tone test we have Vin = A(cos w, t + cos wzt)

.. We have 
$$V_{IF} = \left[\alpha_i A \left(\omega_s \omega_i t + \omega_s \omega_z t\right) + \alpha_i A^2 \left(\omega_s \omega_i t + \omega_s \omega_z t\right)^2\right] \left[\frac{1}{2} + \frac{2}{\pi} \left(\omega_s \omega_i t\right) + \dots\right]$$

for we slightly higher than we, (we -wi) component at the output of the LNA appears at the output of the mixer multiplied by its DC gain and located close to DC.

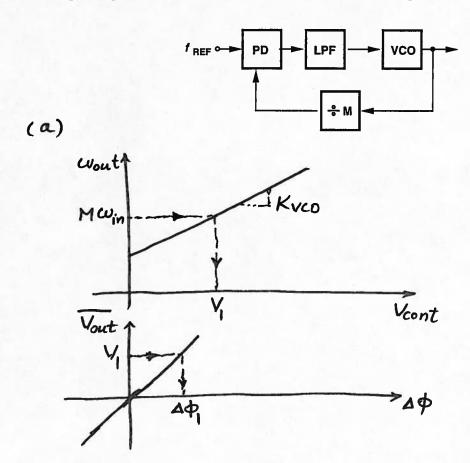
$$\omega_2 - \omega_1$$
: @LO output  $\alpha_2 A^2 \cos(\omega_2 - \omega_1)$ 
@ Mixer output  $\frac{1}{2} \alpha_2 A^2 \cos(\omega_2 - \omega_1)$ 

This resides in the same band as the converted RF signal of A

At the IP2 point 
$$\frac{\alpha_1 A_1 P_2}{\pi} = \frac{\alpha_2 A_1 P_2}{2}$$
  

$$\therefore A_1 P_2 = \frac{\alpha_1}{\alpha_2} \frac{2}{\pi}$$

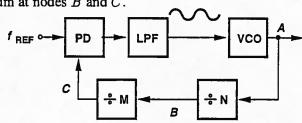
- 4. Consider the simple type-I PLL shown below. Assume the loop is locked.
- (a) If the input frequency is known, describe in detail how you compute the control voltage and the phase error (between the two inputs of the PD).
- (b) If the input frequency changes by  $\Delta\omega$ , compute the change in the phase error.



(b) If the input frequency changes by AW, Wout changes by MAW and the control voltage by MAW. Thus, the input phase error changes by MAW.

KVCOKPD.

- 5. A type-I PLL is shown below, where the control voltage experiences a sinusoidal ripple with a frequency of  $f_{REF}$  and a peak amplitude of  $V_{\tau}$ . Assume the loop is locked.
- (a) Explain the difference between a harmonic and a sideband.
- (b) Using the narrow-band FM approximation, determine the spectrum at point A.
- (c) Now determine the spectrum at nodes B and C.



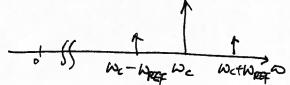
a) HARMONICS - A frequency component in a spectrum that is an integer multiple of the fundamental frequency. Appears when a sinusoid is applied to a non-linear system.

SIDEBANDS - Deterministic frequency component that appears around the carrier frequency. A generic example one tones produced by a modulation process of the original signal.

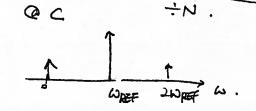
(b) (NOTE) Narrowband FM Approx.

AFM(t) =  $A_c \omega_s [\omega_c t + m]_{-\infty}^t A_{DB}(t) dt$   $\simeq A_c \omega_s \omega_c t - A_c \sin \omega_c t \int A_{DB}(t) dt$ .

Agiven  $A_{DB}(t) = V_r (\omega_s (\omega_{REF} t))$ ,  $\omega_{REF} = 2\pi f_{REF}$ AFM(t)  $\cong A_c \omega_s (\omega_c t) - \frac{A_c V_r m}{\omega_{REF}} \sin (\omega_c t) \sin (\omega_{REF} t)$   $= A_c \omega_s (\omega_c t) - \frac{A_c V_r m}{\omega_{REF}} \omega_s (\omega_c - \omega_{REF}) t + \frac{A_c V_r m}{\omega_{REF}} \omega_s (\omega_c + \omega_{REF}) t_{\omega_c}$ (Spectrum  $(\omega, t)$ )



We Was No tweet



### **EE215C**

## Final Exam Winter 2010

Name: Solutions

Time Limit: 3 Hours
Open Book, Open Notes

- 1. 10
- 2. 10
- 3. 10
- 4. 10

Total: 40

- 1. The circuit shown below is a mixer used in traditional microwave design. Assume when  $M_1$  is on, it has an on-resistance of  $R_{on1}$ . Also, assume abrupt edges and a 50% duty cycle for the LO and neglect channel-length modulation and body effect.
- (a) Compute the voltage conversion gain of the circuit. Assume  $M_2$  does not enter the triode region and denote its transconductance by  $g_{m2}$ .
- (b) If  $R_{on1}$  is very small, determine the  $IP_2$  of the circuit. Assume  $M_2$  has an overdive of  $V_{GS0} V_{TH}$  in the absence of signals (when it is on).

$$V_{RF} \sim \frac{1}{\sqrt{N_1}} \left\{ \begin{array}{c} (a) & -R_0 \\ \hline \frac{1}{\sqrt{N_1}} + R_{ON} \end{array} \right\} \times \frac{2}{\sqrt{L}} \times \frac{1}{\sqrt{L}} \times \frac{1}{\sqrt{$$

(b)

for 2 tone test Vm= Vo cosWat + Vo cosWat

= 2 Mu cox W ( Vois - Voh + Vo cosWit + Vo cosust) 2.

= 2 Mu cox W ( Vois + Voh + Vo coswit + Vo coswit

+ Vo cos (w, -w) t + Vo coscw + w) t - 2 Voso Voh

+ 2 Vo Vois cos W t + 2 Vo Voso cosw 2 t - 2 Vo Woh cosw, t

- 2 Vo Woh cosw 2 t)

the gain for  $W_1 - W_{10} = W_{12} - W_{10}$  at  $V_{2F} = \frac{1}{\pi} \frac{1}{2} M_{11} G_{0x} \frac{W}{L}$ .  $2(V_{615} - V_{614}) R_{1}$  the gain for  $W_1 - W_{12}$  at  $V_{2F} = \frac{1}{2} \frac{1}{2} M_{11} G_{0x} \frac{W}{L}$ .  $R_{D}$ TIP2:  $\frac{4 (V_{615} - V_{614})}{\pi}$ 

- 2. Consider the active mixer shown below, where the LO has abrupt edges and a 50% duty cycle. Also, channel-length modulation and body effect are negligible. The load resistors exhibit mismatch but the circuit is otherwise symmetric. Assume  $M_1$  carries a bias current of  $I_{SS}$ .
- (a) Determine the output offset voltage.
- (b) Determine the  $IP_2$  of the circuit in terms of the overdrive and bias current of  $M_1$ .

$$V_{DD}$$

$$I_{2} = I_{SS} \times S(H)$$

$$V_{LO}$$

$$V_{RF} = I_{2} = I_{2} \times I$$

b) Two-tone test: 
$$V_{RF} = V_1 (o_S \omega_1 t + V_1 CoS \omega_2 t + V_G S_0)$$

$$I_{SS} = \frac{K}{2 P_n G_{SX}(\frac{U}{L})} \left( V_1 (o_S \omega_1 t + V_1 (o_S \omega_2 t + V_G S_0 - V_{TH})^2 \right)$$

Beat Component

$$= K \left\{ V_1^2 \left[ \frac{1 + (o_S 2 \omega_1 t + \frac{1 + (o_S 2 \omega_2 t}{2}) + (o_S \omega_1 + \omega_2) t + (o_S (\omega_1 - \omega_2) t \right] + (V_G S_0 - V_{TH}) \right\} \right\}$$

$$+ 2 (V_G S_0 - V_{TH}) V_1 \left[ (o_S \omega_1 t + (o_S \omega_2 t) \right]$$

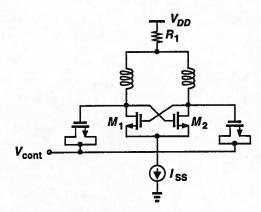
$$V_{SF} = K \left[ 2 (V_G S_0 - V_{TH}) V_1 + R_0 \left( \frac{2}{\alpha} + \alpha \frac{1}{R} \right) \left[ C_0 S(\omega_1 - \omega_1) t + (o_S (\omega_2 - \omega_0) t \right] \right]$$

$$V_{SF} = K \left[ V_1^2 \times \alpha R_0 \times \frac{1}{2} \times (o_S (\omega_1 - \omega_2) t + (o_S \omega_2 t) t + (o_S \omega_2 t) t \right]$$

$$V_{SF} = K \left[ V_1^2 \times \alpha R_0 \times \frac{1}{2} \times (o_S (\omega_1 - \omega_2) t + (o_S \omega_2 t) t + (o_S \omega_2 t) t \right]$$

$$V_{SF} = K \left[ V_1^2 \times \alpha R_0 \times \frac{1}{2} \times (o_S (\omega_1 - \omega_2) t + (o_S \omega_2 t) t + (o$$

- 3. In the VCO circuit shown below, the voltage dependence of each varactor can be expressed as  $C_{var} = C_0(1 + \alpha_1 V_{var})$ , where  $V_{var}$  denotes the average voltage across the varactor. Use the narrowband FM approximation in this problem. Also, neglect all other capacitances and assume the circuit oscillates at a frequency of  $\omega_0$  for the given value of  $V_{cont}$ . The dc drop across the inductors is negligible.
- (a) Compute the "gain" from  $I_{SS}$  to the output frequency,  $\omega_{out}$ . That is, assume  $I_{SS}$  changes by a small value and calculate the voltage change across the varactors and hence the change in the output frequency.
- (b) Assume  $I_{SS}$  has a noise component that can be expressed as  $I_n \cos \omega_n t$ . Using the result found in (a), determine the frequency and relative magnitude of the resulting output sidebands of the oscillator.



$$\omega_{\text{out}} = \frac{1}{\sqrt{LC_{\text{out}}}} = \frac{1}{\sqrt{LC_{\text{o}}(1+d_1V_{\text{ven}})}}$$

$$\frac{2}{\sqrt{LC_{\text{o}}}} \left(1 - \frac{d_1V_{\text{ven}}}{2}\right) = \omega_{\text{o}}\left(1 - \frac{d_1V_{\text{ven}}}{2}\right)$$

$$\frac{1}{\sqrt{LC_{\text{o}}}} \left(1 - \frac{d_1V_{\text{ven}}}{2}\right) = \omega_{\text{o}}\left(1 - \frac{d_1V_{\text{ven}}}{2}\right)$$

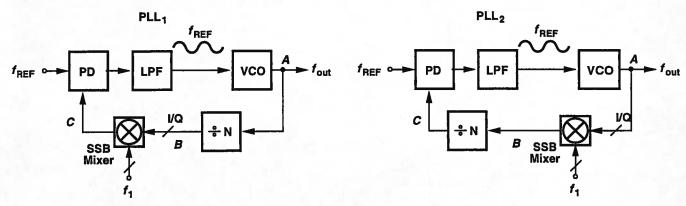
(b) Using NB Approx.

$$V_{cut}(t) = A \omega_{s} \left( \omega_{o}t + \frac{\alpha_{s}\omega_{c}R_{i}}{2\omega_{n}} \cdot I_{n} \sin(\omega_{n}t) \right)$$

$$\stackrel{\sim}{=} A \omega_{s} (\omega_{c}t) - \frac{A \alpha_{s}\omega_{c}R_{i}I_{n}}{4\omega_{n}} \omega_{s} (\omega_{c}-\omega_{o})t + \frac{A\alpha_{s}\omega_{c}R_{i}I_{n}}{4\omega_{n}} \omega_{s} (\omega_{c}-\omega_{o})t + \frac{A\alpha_{s}\omega_{c}R_{i}I_{n}}{4\omega_{n}} \omega_{s} (\omega_{c}-\omega_{o})t$$

$$(60) \text{ freq. } \omega_{c} \pm \omega_{n}$$

- 4. Two PLL configurations are shown below. Assume the SSB mixer adds its input frequencies. Also, assume  $f_1$  is a constant frequency provided externally and it is less than  $f_{REF}$ . The control voltage experiences a small sinusoidal ripple with a frequency of  $f_{REF}$ . Both PLLs are locked.
- (a) Determine the output frequencies of the two PLLs.
- (b) Determine the spectrum at point A due to the ripple.
- (c) Now determine the spectrum at nodes B and C.



(a) PLL: 
$$\frac{f_{out}}{N} + f_1 = f_{REF} \Rightarrow f_{out} = N(f_{REF} - f_1)$$

