EE 215D

Spring 2012

Analog Microsystem Design

Instructor:  Behzad Razavi
            56-147D, Eng. IV
            (310) 206-1633, razavi@ee.ucla.edu
            Office Hours: Tue., Thur., 1:30-3:00 pm

Time:       TR, 4:00-5:50 pm

Place:      BL8500

Prerequisites: EE215A

Credit:     4 Units

Note: No use of laptops is allowed during lecture.

Grading:    Midterm 30%
            Final 30%
            Homeworks 20% (Late HW policy: 25%/day deduction)
            (Unstapled homeworks will not be graded.)
            Final Project 20%

Course Textbook:

Reference Books:

Important Dates:
Thur. April 12            HW#1 Due
Tue. April 24             HW#2 Due
Tue. May 1                HW#3 Due
Tue. May 8                HW#4 Due
Thur. May 10              Midterm Exam
Fri. June 8               Final Project Due
Mon., June 11, 6:30-9:30 pm Final Exam
Outline

- Introduction to Switched-Capacitor Circuits
- Sampling Circuits and Architectures
- Digital-to-Analog Conversion
- Analog-to-Digital Conversion
- Oversampled Converters
- Building Blocks
- Precision Techniques