

Advances in Protograph-Based LDPC Codes and a Rate Allocation Problem

PhD Defense

Sudarsan Vasista Srinivasan Ranganathan

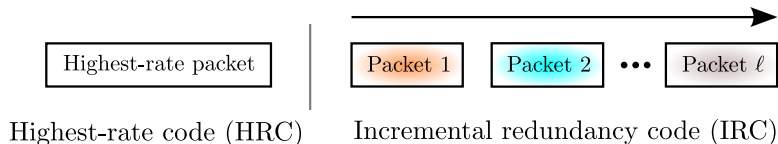
Advisors: Richard D. Wesel and Dariush Divsalar

Communication Systems Laboratory
University of California, Los Angeles

- 1 Incremental Redundancy (IR) and LDPC Codes
- 2 Quasi-Cyclic PBRL Design for Short Block-Lengths
- 3 A Property of PBRL Decoding
- 4 PBRL Codes for Universal Increment Ordering
- 5 A Rate Allocation Problem

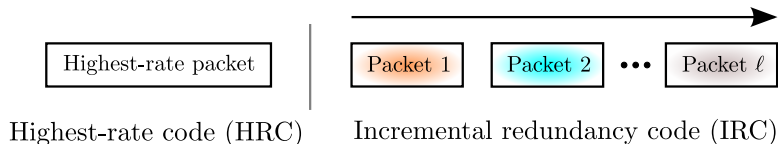
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 - Rate-Compatible Codes, Protograph LDPC Codes
 - Protograph QC-LDPC Codes
- 2 Quasi-Cyclic PBRL Design for Short Block-Lengths
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Rate-Compatibility (RC)



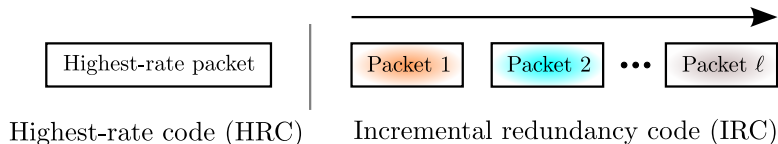
- Useful in many communication applications

Rate-Compatibility (RC)



- Useful in many communication applications
- RC codes most recently proposed for the **5G wireless standard**

Rate-Compatibility (RC)



- Useful in many communication applications
- RC codes most recently proposed for the **5G wireless standard**
- **Challenge today:** short block-lengths, ever-growing throughput requirements, new applications, handling rate-compatibility

H of size 6×9

$$H = \begin{bmatrix} 1 & 0 & 0 & 1 & 0 & 0 & 1 & 0 & 0 \\ 0 & 1 & 0 & 0 & 1 & 0 & 0 & 1 & 0 \\ 0 & 0 & 1 & 0 & 0 & 1 & 0 & 0 & 1 \\ 1 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 1 \\ 0 & 1 & 0 & 0 & 0 & 1 & 1 & 0 & 0 \\ 0 & 0 & 1 & 1 & 0 & 0 & 0 & 1 & 0 \end{bmatrix}$$

Variable nodes

H of size 6×9

$$H = \begin{bmatrix} 1 & 0 & 0 & 1 & 0 & 0 & 1 & 0 & 0 \\ 0 & 1 & 0 & 0 & 1 & 0 & 0 & 1 & 0 \\ 0 & 0 & 1 & 0 & 0 & 1 & 0 & 0 & 1 \\ 1 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 1 \\ 0 & 1 & 0 & 0 & 0 & 1 & 1 & 0 & 0 \\ 0 & 0 & 1 & 1 & 0 & 0 & 0 & 1 & 0 \end{bmatrix}$$



Linear Codes \iff Tanner Graphs

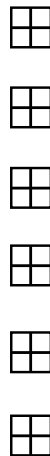
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Variable nodes



Check nodes



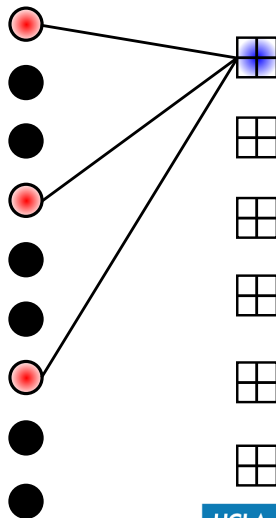
Linear Codes \iff Tanner Graphs

H of size 6×9

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Variable nodes

Check nodes



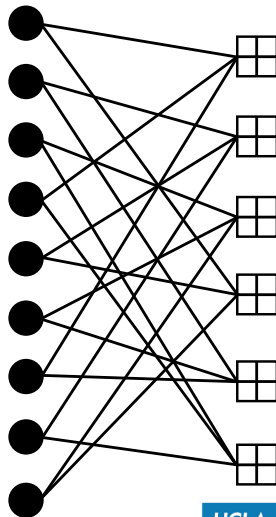
Linear Codes \iff Tanner Graphs

H of size 6×9

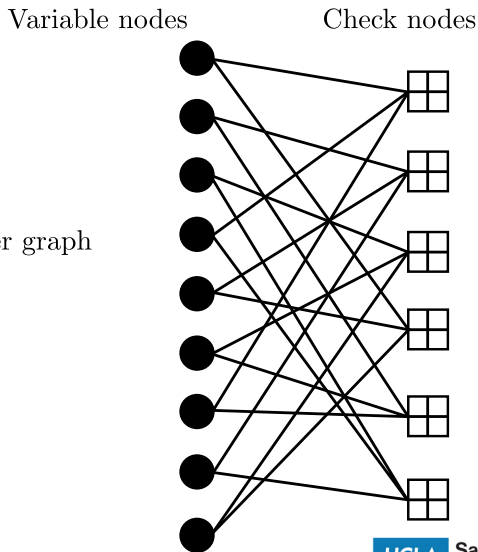
$$H = \begin{bmatrix} 1 & 0 & 0 & 1 & 0 & 0 & 1 & 0 & 0 \\ 0 & 1 & 0 & 0 & 1 & 0 & 0 & 1 & 0 \\ 0 & 0 & 1 & 0 & 0 & 1 & 0 & 0 & 1 \\ 1 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 1 \\ 0 & 1 & 0 & 0 & 0 & 1 & 1 & 0 & 0 \\ 0 & 0 & 1 & 1 & 0 & 0 & 0 & 1 & 0 \end{bmatrix}$$

Variable nodes

Check nodes



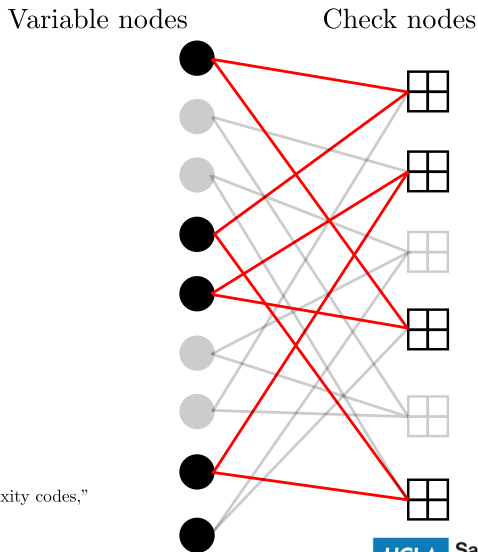
“Regular” bipartite Tanner graph
Variable node degree 2
Check node degree 3



Linear Codes \iff Tanner Graphs

Girth:
Length of smallest cycle
in graph

R. Tanner, "A recursive approach to low complexity codes,"
IEEE Trans. Inf. Theory, Sep. 1981.



Low-Density Parity-Check Codes (LDPC Codes)

- Most important class of codes treated as graphs are **LDPC codes**

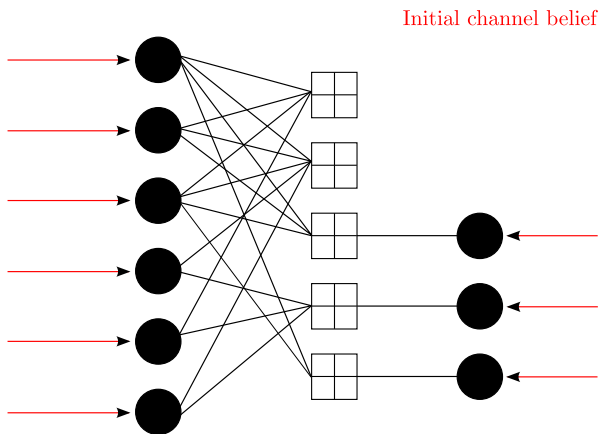
Robert Gray Gallager, "Low-Density Parity-Check Codes," *MIT Press*, 1963.

Low-Density Parity-Check Codes (LDPC Codes)

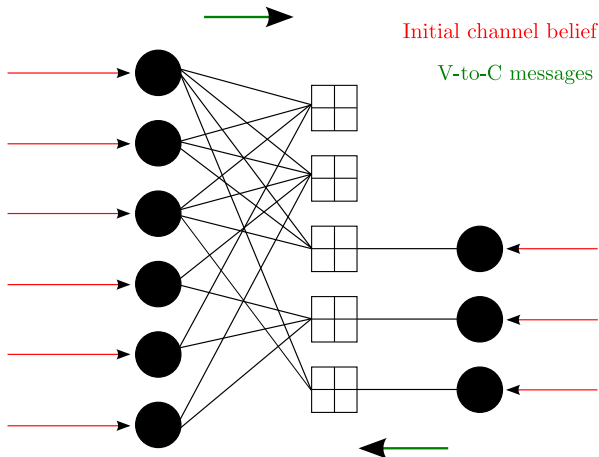
- Most important class of codes treated as graphs are **LDPC codes**
- LDPC: parity-check matrix is of “low density”

Robert Gray Gallager, “Low-Density Parity-Check Codes,” *MIT Press*, 1963.

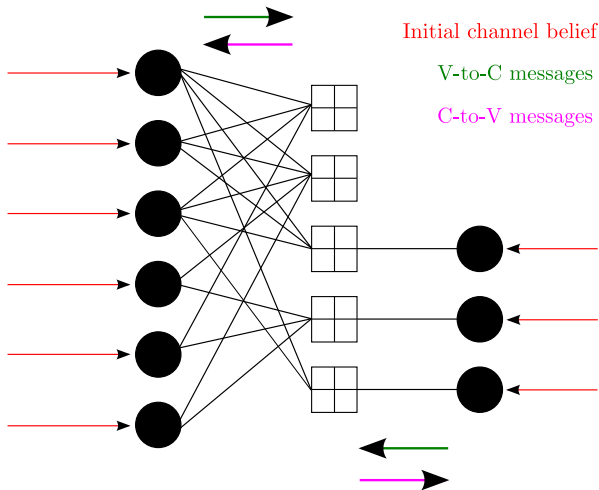
Iterative Decoding of (LDPC) Codes



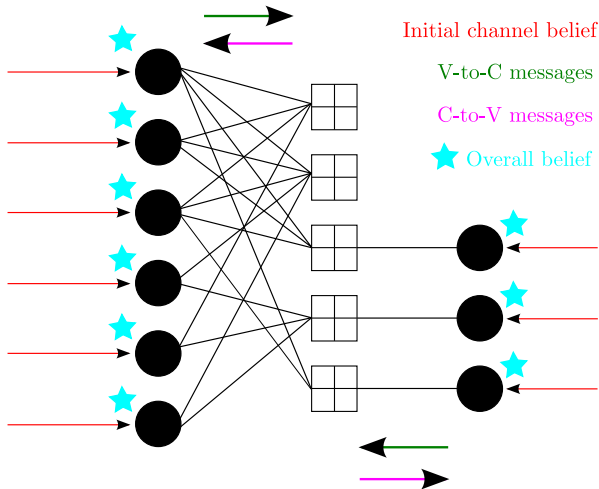
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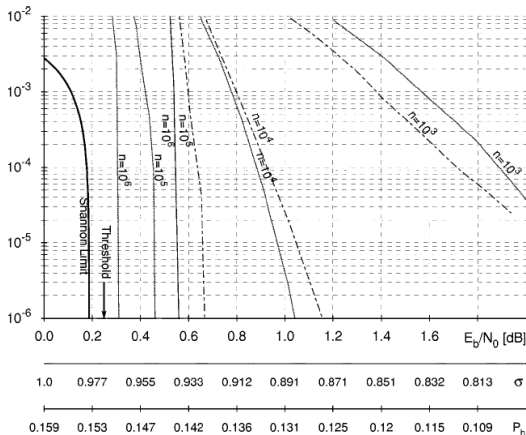
Iterative Decoding of (LDPC) Codes



Iterative Decoding of (LDPC) Codes



Iterative Decoding Threshold



Reproduced from T. J. Richardson and R. L. Urbanke, "Design of capacity-approaching irregular low-density parity-check codes," *IEEE Trans. Inf. Theory*, Feb. 2001.

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Definition (Circulant permutation matrix (CPM))

An $N \times N$ **circulant permutation matrix (CPM)**, x -shifted ($0 \leq x \leq N - 1$) is

- the identity matrix cyclically shifted by x positions

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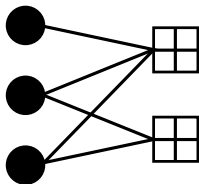
- the identity matrix cyclically shifted by x positions

Example, when $x = 2$, $N = 4$, right shift:

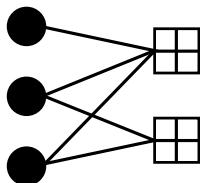
$$\begin{bmatrix} 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 1 \\ 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 \end{bmatrix}$$

Protograph

A small Tanner graph



Protograph
A small Tanner graph



Corresponding
Protomatrix

$$P = \begin{bmatrix} 1 & 1 & 1 \\ 0 & 1 & 1 \end{bmatrix}$$

$$n_c = 2, n_v = 3$$

$$R = \frac{n_v - n_c}{n_v - n_p}$$

Jeremy Thorpe, "Low-density parity-check (LDPC) codes constructed from protographs," *IPN-PR 42-154*, JPL, Aug. 2003.

Protograph Quasi-Cyclic LDPC (Protograph QC-LDPC) Codes

$$\begin{bmatrix} 1 & 1 & 1 \\ 1 & 1 & 1 \end{bmatrix}$$

Protomatrix of
complete protograph

Protograph Quasi-Cyclic LDPC (Protograph QC-LDPC) Codes

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Protomatrix of
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CPM blocks $N \times N$ in
place of each 1

$$\left[\begin{array}{cc|cc|cc} 1 & 0 & 1 & 0 & 1 & 0 \\ 0 & 1 & 0 & 1 & 0 & 1 \\ \hline 1 & 0 & 1 & 0 & 0 & 1 \\ 0 & 1 & 0 & 1 & 1 & 0 \end{array} \right]$$

Protograph Quasi-Cyclic LDPC (Protograph QC-LDPC) Codes

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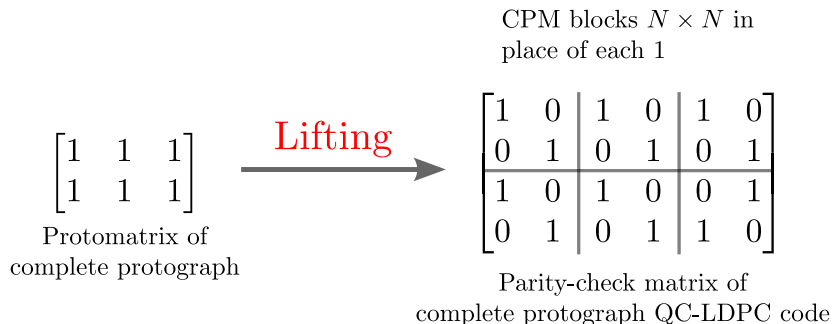


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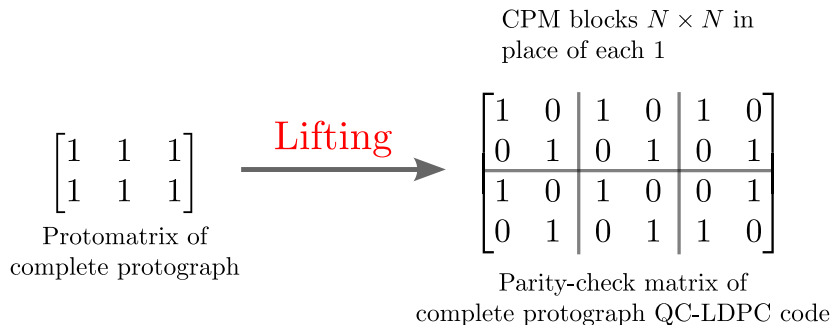
$$\left[\begin{array}{cc|cc|cc} 1 & 0 & 1 & 0 & 1 & 0 \\ 0 & 1 & 0 & 1 & 0 & 1 \\ \hline 1 & 0 & 1 & 0 & 0 & 1 \\ 0 & 1 & 0 & 1 & 1 & 0 \end{array} \right]$$

Parity-check matrix of
complete protograph QC-LDPC code

Protograph Quasi-Cyclic LDPC (Protograph QC-LDPC) Codes



Protograph Quasi-Cyclic LDPC (Protograph QC-LDPC) Codes



Lifting factor N

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S. V. S. Ranganathan, D. Divsalar, and R. D. Wesel, "Design of improved quasi-cyclic protograph-based raptor-like LDPC codes for short block-lengths," In *Proc. IEEE Int. Symp. Inform. Theory (ISIT)*, Jun. 2017.

S. V. S. Ranganathan, D. Divsalar, and R. D. Wesel, "Quasi-cyclic protograph-based raptor-like LDPC codes for short block-lengths," Under revision, *IEEE Trans. Inf. Theory*.

- Best performing RC-LDPC codes known so far

Tsung-Yi Chen, K. Vakilinia, D. Divsalar, and R. D. Wesel, "Protograph-based raptor-like LDPC codes," *IEEE Trans. Commun.*, May 2015.

- Best performing RC-LDPC codes known so far
 - The code family in 5G for data communication

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Protograph-Based Raptor-Like (PBRL) Codes

- Best performing RC-LDPC codes known so far
 - The code family in 5G for data communication
- General structure of a PBRL protomatrix

$$P = \begin{bmatrix} P_{\text{HRC}} & 0 \\ P_{\text{IRC}} & I \end{bmatrix}_{n_c \times n_v} \quad (1)$$

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- $P_{\text{HRC}} = \text{HRC (highest-rate code) part}$

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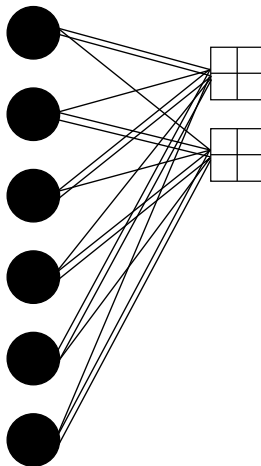
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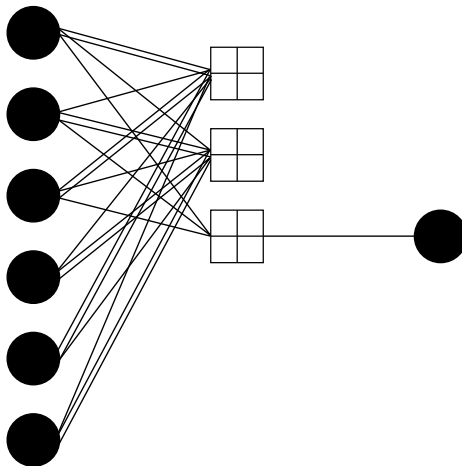
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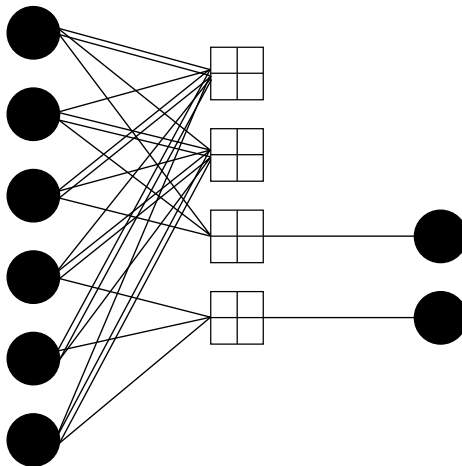
- P_{HRC} = HRC (highest-rate code) part
- P_{IRC} = IRC (incremental redundancy code) part

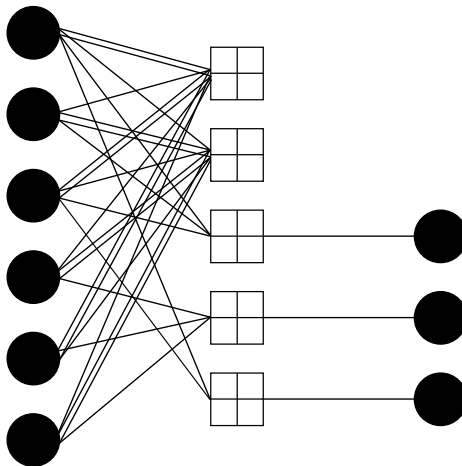
Tsung-Yi Chen, K. Vakilinia, D. Divsalar, and R. D. Wesel, "Protograph-based raptor-like LDPC codes," *IEEE Trans. Commun.*, May 2015.

$$\begin{array}{c}
 n_{c_H} = 2, n_{v_H} = 6 \\
 P_{\text{HRC}} \\
 P = \left[\begin{array}{cc|cc|cc|ccc}
 2 & 1 & 2 & 1 & 2 & 1 & 0 & 0 & 0 \\
 1 & 2 & 1 & 2 & 1 & 2 & 0 & 0 & 0 \\
 \hline
 1 & 1 & 1 & 0 & 0 & 0 & 1 & 0 & 0 \\
 0 & 0 & 0 & 1 & 1 & 1 & 0 & 1 & 0 \\
 1 & 0 & 1 & 0 & 0 & 0 & 0 & 0 & 1
 \end{array} \right] \\
 P_{\text{IRC}} \quad n_c = 5, n_v = 9
 \end{array}$$









$$P = \begin{bmatrix} 2 & 1 & 2 & 1 & 2 & 1 & 2 & 1 \\ 1 & 2 & 1 & 2 & 1 & 2 & 1 & 2 \end{bmatrix} \quad \begin{matrix} P_{\text{HRC}} \\ \\ \\ \\ \\ \\ \\ P_{\text{IRC}} \end{matrix} \quad \text{Rate 6/7, Threshold} = 3.07\text{dB}$$

Original PBRL Design Procedure

$$P = \left[\begin{array}{ccccccccc|c} & P_{\text{HRC}} \\ 2 & 1 & 2 & 1 & 2 & 1 & 2 & 1 & 0 \\ 1 & 2 & 1 & 2 & 1 & 2 & 1 & 2 & 0 \\ \hline 1 & 0 & 1 & 0 & 1 & 0 & 0 & 0 & 1 \\ & P_{\text{IRC}} \end{array} \right] \quad \begin{array}{l} \text{Rate } 6/7, \text{ Threshold} = 3.07\text{dB} \\ \text{Rate } 6/8, \text{ Threshold} = 2.11\text{dB} \end{array}$$

$$P = \begin{array}{c} P_{\text{HRC}} \\ \left[\begin{array}{cccccc|cc} 2 & 1 & 2 & 1 & 2 & 1 & 2 & 1 & 0 & 0 \\ 1 & 2 & 1 & 2 & 1 & 2 & 1 & 2 & 0 & 0 \\ \hline 1 & 0 & 1 & 0 & 1 & 0 & 0 & 0 & 1 & 0 \\ 1 & 0 & 1 & 0 & 1 & 0 & 1 & 0 & 0 & 1 \end{array} \right] \\ P_{\text{IRC}} \end{array}$$

Rate 6/7, Threshold = 3.07dB
 Rate 6/8, Threshold = 2.11dB
 Rate 6/9, Threshold = 1.61dB

$$P = \begin{array}{c} P_{\text{HRC}} \\ \left[\begin{array}{cccccc|ccc} 2 & 1 & 2 & 1 & 2 & 1 & 2 & 1 & 0 & 0 & 0 \\ 1 & 2 & 1 & 2 & 1 & 2 & 1 & 2 & 0 & 0 & 0 \\ \hline 1 & 0 & 1 & 0 & 1 & 0 & 0 & 0 & 1 & 0 & 0 \\ 1 & 0 & 1 & 0 & 1 & 0 & 1 & 0 & 0 & 1 & 0 \\ 1 & 1 & 1 & 0 & 1 & 0 & 1 & 0 & 0 & 0 & 1 \end{array} \right] \\ P_{\text{IRC}} \end{array}$$

Rate 6/7, Threshold = 3.07dB
 Rate 6/8, Threshold = 2.11dB
 Rate 6/9, Threshold = 1.61dB
 6/10, Threshold = 1.17dB

$$P = \begin{array}{c} P_{\text{HRC}} \\ \left[\begin{array}{cccccc|cccc} 2 & 1 & 2 & 1 & 2 & 1 & 2 & 1 & 0 & 0 & 0 & 0 & 0 \\ 1 & 2 & 1 & 2 & 1 & 2 & 1 & 2 & 0 & 0 & 0 & 0 & 0 \\ \hline 1 & 0 & 1 & 0 & 1 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 \\ 1 & 0 & 1 & 0 & 1 & 0 & 1 & 0 & 0 & 1 & 0 & 0 & 0 \\ 1 & 1 & 1 & 0 & 1 & 0 & 1 & 0 & 0 & 0 & 1 & 0 & 0 \\ & & & \vdots & & & & & & \vdots & & & \\ & & & \vdots & & & & & & \vdots & & & \\ & & & \vdots & & & & & & \vdots & & & \end{array} \right] \\ P_{\text{IRC}} \end{array} \begin{array}{l} \text{Rate 6/7, Threshold} = 3.07\text{dB} \\ \text{Rate 6/8, Threshold} = 2.11\text{dB} \\ \text{Rate 6/9, Threshold} = 1.61\text{dB} \\ \text{6/10, Threshold} = 1.17\text{dB} \end{array}$$

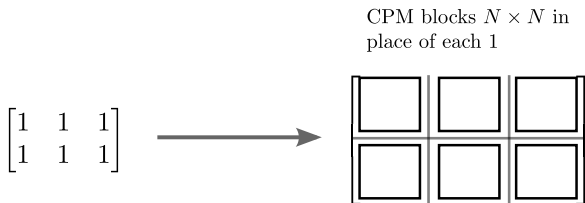
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Why a New Design Method?

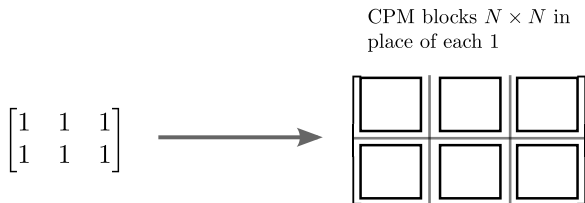
- Threshold \iff long-block-length property

- Threshold \iff long-block-length property
 - Cannot be used for **low FER requirements at short block-lengths**

A Minimum Distance Upper Bound



A Minimum Distance Upper Bound



Theorem (Permanent Bound)

The minimum distance of a protograph QC-LDPC code is upper bounded by a constant that does not depend upon N and that depends only upon the protograph.

R. Smarandache and P. O. Vontobel, "Quasi-cyclic LDPC codes: Influence of proto- and tanner-graph structure on minimum hamming distance upper bounds," *IEEE Trans. Inf. Theory*, Feb. 2012.

Theorem (Complexity of Permanent Bound)

For a protomatrix of size $n_c \times n_v$, the complexity of computing this upper bound is $\Theta\left(\binom{n_v}{n_c+1} (n_c + 1) \cdot n_c 2^{n_c}\right)$.

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Note the dependence on n_c, n_v (the size of the whole protomatrix)

R. Smarandache and P. O. Vontobel, "Quasi-cyclic LDPC codes: Influence of proto- and tanner-graph structure on minimum hamming distance upper bounds," *IEEE Trans. Inf. Theory*, Feb. 2012.

Permanent Bound Design (PBD) Method

$$P = \left[\begin{array}{cccccc|c} & P_{\text{HRC}} \\ 2 & 1 & 2 & 1 & 2 & 1 & 2 & 1 \\ 1 & 2 & 1 & 2 & 1 & 2 & 1 & 2 \\ \hline & & & & & & & \end{array} \right]$$

Permanent Bound Design (PBD) Method

$$P = \begin{array}{c} P_{\text{HRC}} \\ \left[\begin{array}{cccccc|c} 2 & 1 & 2 & 1 & 2 & 1 & 2 & 1 & 0 \\ 1 & 2 & 1 & 2 & 1 & 2 & 1 & 2 & 0 \\ \hline 1 & 1 & 1 & 0 & 0 & 0 & 0 & 0 & 1 \end{array} \right] \\ P_{\text{IRC}} \end{array} \quad \begin{array}{l} \text{Rate } 6/7, \text{ Min. Dist. Upper Bound} = 8 \\ \text{Rate } 6/8, \text{ Min. Dist. Upper Bound} = 12 \end{array}$$

Permanent Bound Design (PBD) Method

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Permanent Bound Design (PBD) Method

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Rate 6/7, Min. Dist. Upper Bound = 8

Rate 6/8, Min. Dist. Upper Bound = 12

Rate 6/9, Min. Dist. Upper Bound = 14

Complexity Reduction for PBRL Protomatrices

$$\left[\begin{array}{c|c} n_{c_H} \times n_{v_H} & \\ \hline & \end{array} \right]_{n_c \times n_v}$$

Theorem (Permanent Bound Complexity for PBRL Protomatrices)

For a PBRL protomatrix of size $n_c \times n_v$, the complexity of computing the permanent upper bound is $\Theta \left(\binom{n_{v_H}}{n_{c_H}+1} (n_c + 1) \cdot (n_{c_H} + 1) 2^{(n_{c_H}+1)} \right)$.

Complexity Reduction for PBRL Protomatrices

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Recall, it was previously $\Theta \left(\binom{n_v}{n_c+1} (n_c + 1) \cdot n_c 2^{n_c} \right)$

Reduction in Complexity of Design Algorithm

[illegible]

Reduction in Complexity of Design Algorithm

$$P = \begin{array}{c} P_{\text{HRC}} \\ \left[\begin{array}{cccccc|c} 2 & 1 & 2 & 1 & 2 & 1 & 2 & 1 & 0 \\ 1 & 2 & 1 & 2 & 1 & 2 & 1 & 2 & 0 \\ \hline 1 & 1 & 1 & 0 & 0 & 0 & 0 & 0 & 1 \end{array} \right] \\ P_{\text{RC}} \end{array} \quad \begin{array}{l} \text{Rate 6/7, Min. Dist. Upper Bound} = 8 \\ \text{Rate 6/8, Min. Dist. Upper Bound} = 12 \end{array}$$

Reduction in Complexity of Design Algorithm

$$P = \begin{array}{c} P_{\text{HRC}} \\ \left[\begin{array}{cccccc|cc} 2 & 1 & 2 & 1 & 2 & 1 & 2 & 1 & 0 & 0 \\ 1 & 2 & 1 & 2 & 1 & 2 & 1 & 2 & 0 & 0 \\ \hline 1 & 1 & 1 & 0 & 0 & 0 & 0 & 0 & 1 & 0 \\ 1 & 0 & 0 & 1 & 0 & 0 & 1 & 0 & 0 & 1 \end{array} \right] \\ P_{\text{RC}} \end{array} \quad \begin{array}{l} \text{Rate 6/7, Min. Dist. Upper Bound} = 8 \\ \text{Rate 6/8, Min. Dist. Upper Bound} = 12 \\ \text{Rate 6/9, Min. Dist. Upper Bound} = 14 \end{array}$$

Reduction in Complexity of Design Algorithm

$$P = \begin{array}{c} P_{\text{HRC}} \\ \left[\begin{array}{cccccc|cccc} 2 & 1 & 2 & 1 & 2 & 1 & 2 & 1 & 0 & 0 & 0 & 0 & 0 \\ 1 & 2 & 1 & 2 & 1 & 2 & 1 & 2 & 0 & 0 & 0 & 0 & 0 \\ \hline 1 & 1 & 1 & 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 \\ 1 & 0 & 0 & 1 & 0 & 0 & 1 & 0 & 0 & 1 & 0 & 0 & 0 \\ \vdots & & & & & & & & \vdots & & & & \\ \vdots & & & & & & & & \vdots & & & & \\ \vdots & & & & & & & & \vdots & & & & \end{array} \right] \\ P_{\text{IRC}} \end{array}$$

Rate 6/7, Min. Dist. Upper Bound = 8

Rate 6/8, Min. Dist. Upper Bound = 12

Rate 6/9, Min. Dist. Upper Bound = 14

Turns out, we can do better!

Theorem (Reduced Complexity Design Algorithm)

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- *The complexity of a “pre-compute” step is*

$$\Theta \left(\binom{n_{v_H}}{n_{c_H}+1} (n_{c_H} + 1) \cdot n_{c_H} 2^{n_{c_H}} + \binom{n_{v_H}}{n_{c_H}+1} \cdot (n_{c_H} + 1) 2^{(n_{c_H}+1)} \right).$$

Theorem (Reduced Complexity Design Algorithm)

- The complexity of a “pre-compute” step is $\Theta \left(\binom{n_{vH}}{n_{cH}+1} (n_{cH} + 1) \cdot n_{cH} 2^{n_{cH}} + \binom{n_{vH}}{n_{cH}+1} \cdot (n_{cH} + 1) 2^{(n_{cH}+1)} \right)$.
- For the design rows, the complexity is $O \left(\binom{n_{vH}}{n_{cH}+1} n_{vH} \right)$.

Theorem (Design of a Row \iff ILP)

The design of one row of the IRC part according to the PBD method is an integer linear program with $\text{dom} = \{\text{candidate rows}\}$.

Theorem (ILP \neq LP relaxation)

The relaxation of the ILP is not exact.

The LP Relaxation is Still Useful

$$\left[\begin{array}{c|c} n_{c_H} \times n_{v_H} & \\ \hline & \end{array} \right]_{n_c \times n_v}$$

Theorem (New Upper Bound for PBRL Protomatrices)

The LP Relaxation is Still Useful

$$\left[\begin{array}{c|c} n_{cH} \times n_{vH} & \\ \hline & \end{array} \right]_{n_c \times n_v}$$

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- *For a given HRC part, the LP relaxation provides a new set of upper bounds at all lower design rates.*

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Theorem (New Upper Bound for PBRL Protomatrices)

- *For a given HRC part, the LP relaxation provides a new set of upper bounds at all lower design rates.*
- *These upper bounds can be obtained without even having to go through the actual design procedure.*

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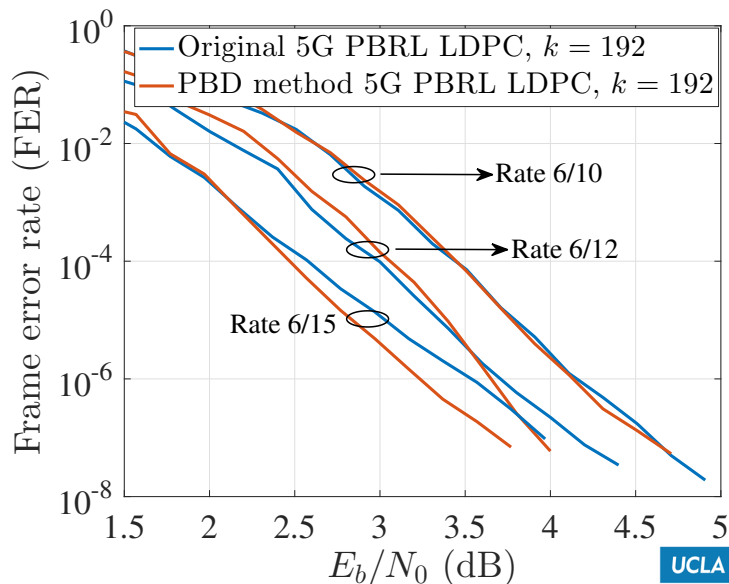
New PBRL Protograph for 5G at 192 Information Bits

$$\begin{bmatrix} 1 & 1 & 1 & 1 & 0 & 0 & 1 & 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 1 & 0 & 0 & 1 & 1 & 1 & 0 & 1 & 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\ 1 & 1 & 0 & 1 & 1 & 0 & 1 & 0 & 1 & 1 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 1 & 1 & 0 & 1 & 1 & 1 & 0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 & 0 \\ 1 & 1 & 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 \\ 1 & 1 & 0 & 0 & 0 & 1 & 0 & 1 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 \\ 1 & 0 & 0 & 0 & 0 & 1 & 0 & 1 & 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 \\ 0 & 1 & 0 & 0 & 0 & 1 & 0 & 1 & 0 & 1 & 0 & 0 & 0 & 1 & 0 & 0 \\ 1 & 1 & 0 & 0 & 0 & 0 & 0 & 0 & 1 & 0 & 0 & 0 & 0 & 0 & 1 & 0 \\ 0 & 1 & 0 & 0 & 0 & 0 & 1 & 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 \\ 1 & 1 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 1 \end{bmatrix}$$

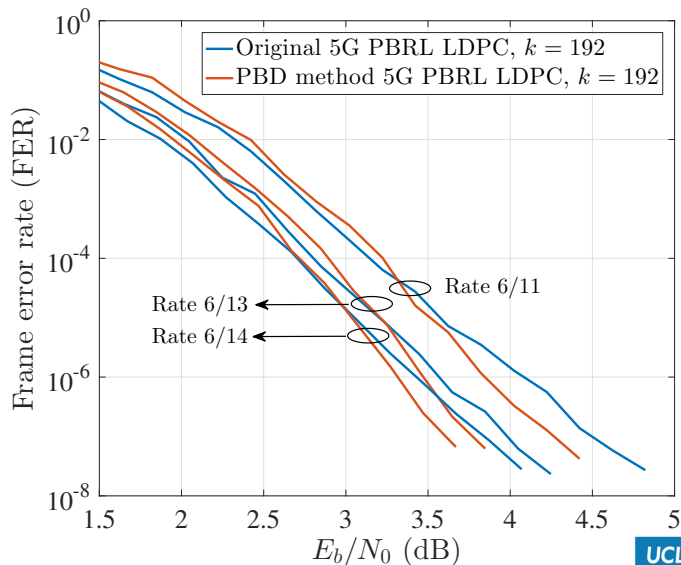
New PBRL Protograph for 5G at 192 Information Bits

1	1	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0
1	0	0	1	1	1	0	1	1	0	0	0	0	0	0	0	0	0
1	1	0	1	1	0	1	0	1	1	0	0	0	0	0	0	0	0
0	1	1	0	1	1	1	0	0	1	0	0	0	0	0	0	0	0
1	1	0	0	0	1	0	0	0	0	1	0	0	0	0	0	0	0
1	1	1	0	0	0	0	0	1	0	0	1	0	0	0	0	0	0
1	0	0	0	0	0	0	1	0	1	0	0	1	0	0	0	0	0
0	1	0	0	1	1	0	1	0	0	0	0	0	1	0	0	0	0
1	1	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	0
0	1	0	0	0	1	0	0	0	1	0	0	0	0	0	1	0	0
1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1

New Codes for 5G at 192 Information Bits



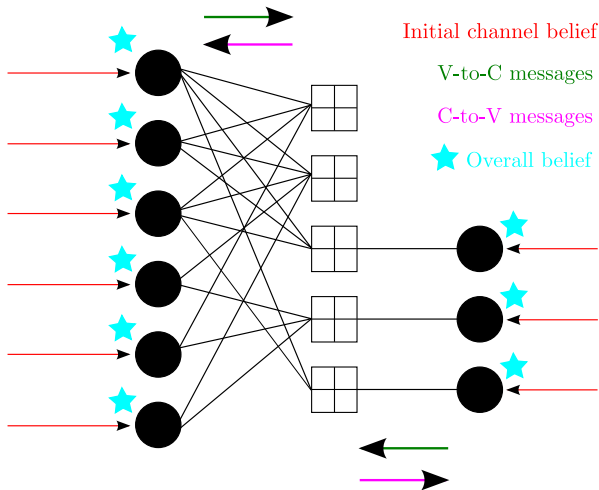
New Codes for 5G at 192 Information Bits



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S. V. S. Ranganathan, R. D. Wesel, and D. Divsalar, “Linear rate-compatible codes with degree-1 extending variable nodes under iterative decoding,” In *Proc. IEEE Int. Symp. Inform. Theory (ISIT)*, Jun. 2018.

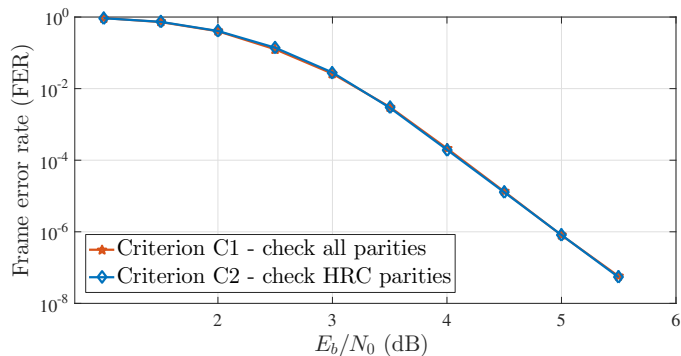
Recall LDPC Decoding...



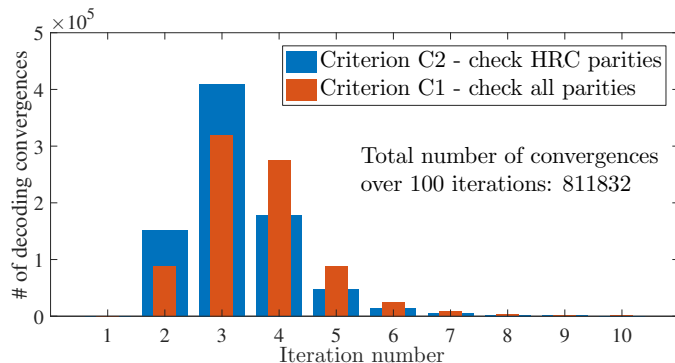
Theorem

It is sufficient to check whether the HRC variable nodes have converged to a codeword.

Checking Only HRC Parities – No Penalty

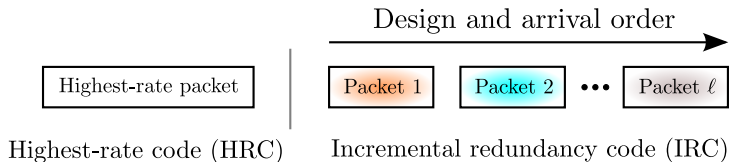


Checking Only HRC Parities – Faster Convergence

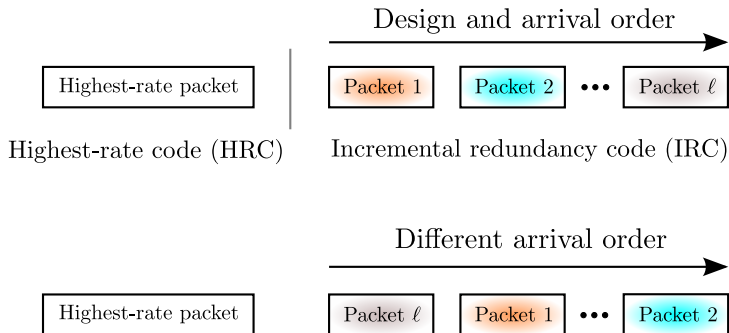


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“Universal rate-compatible LDPC code families for any increment
ordering,” In *Proc. 9th Int. Symp. Turbo Codes & Iterative Inf. Processing
(ISTC)*, Sep. 2016.



Incremental Arrival Order



- **Metric 1:** Require every order of arrival have same performance

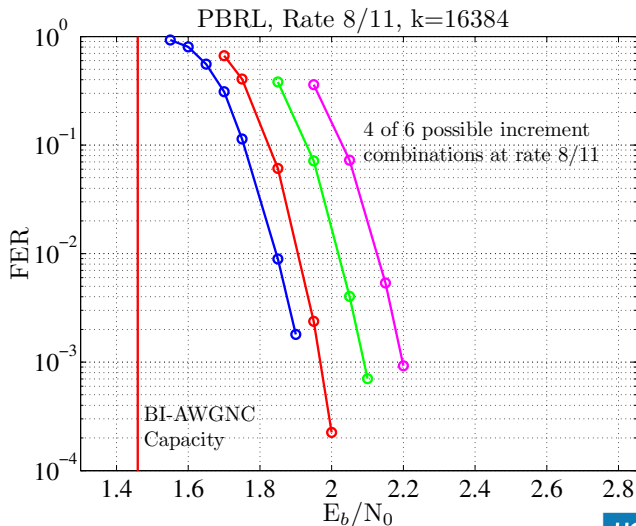
- **Metric 1:** Require every order of arrival have same performance
 - Inspired by requirement of inter-frame coding

H. Wang, **S. V. S. Ranganathan**, and R. D. Wesel, "Approaching capacity using incremental redundancy without feedback," In *Proc. IEEE Int. Symp. Inform. Theory (ISIT)*, Jun. 2017.

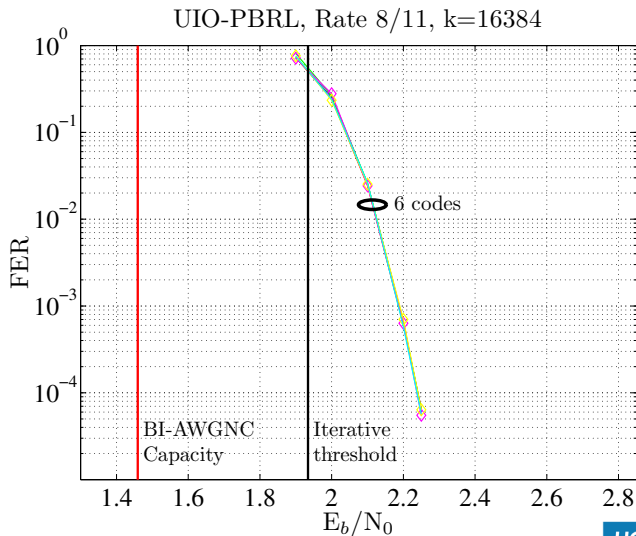
- **Metric 1:** Require every order of arrival have same performance
 - Inspired by requirement of inter-frame coding
- **Metric 2:** Require best throughput as you decode in a feedback system

H. Wang, **S. V. S. Ranganathan**, and R. D. Wesel, "Approaching capacity using incremental redundancy without feedback," In *Proc. IEEE Int. Symp. Inform. Theory (ISIT)*, Jun. 2017.

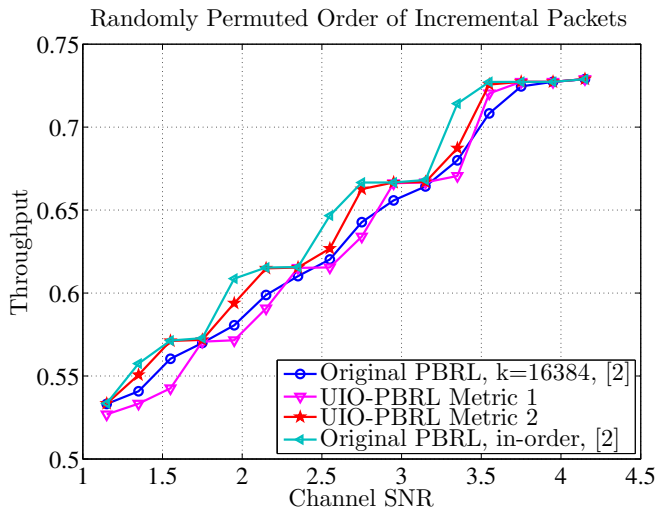
Original PBRL Code – Different Arrival Orderings



UIO-PBRL Code Designed for Metric 1



UIO-PBRL Code Designed for Metric 2



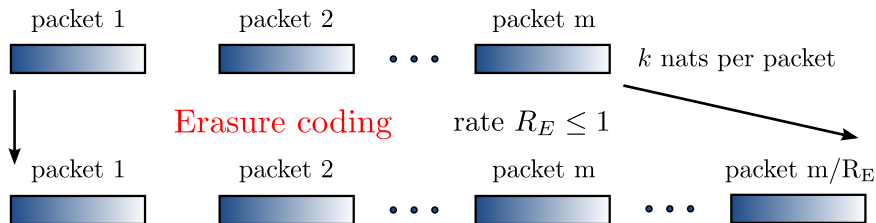
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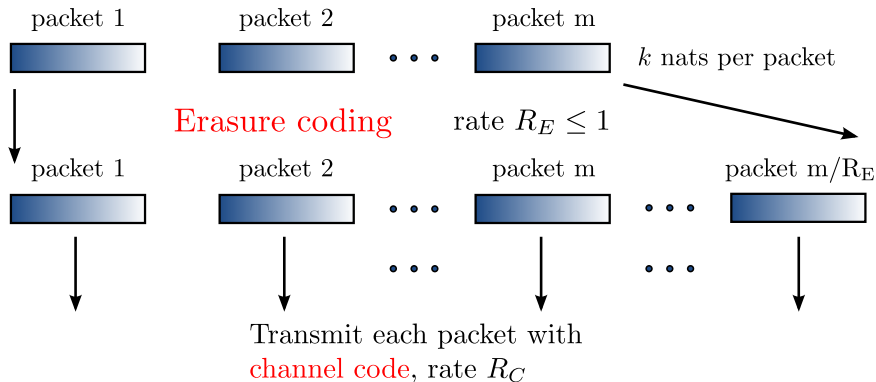
Problem Setup



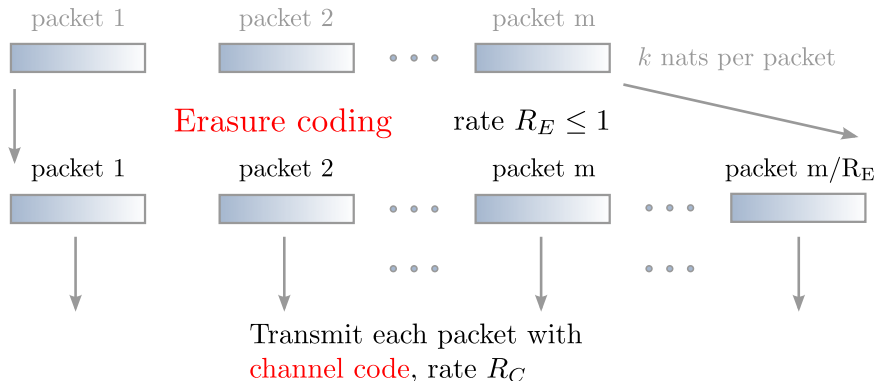
Problem Setup



Problem Setup

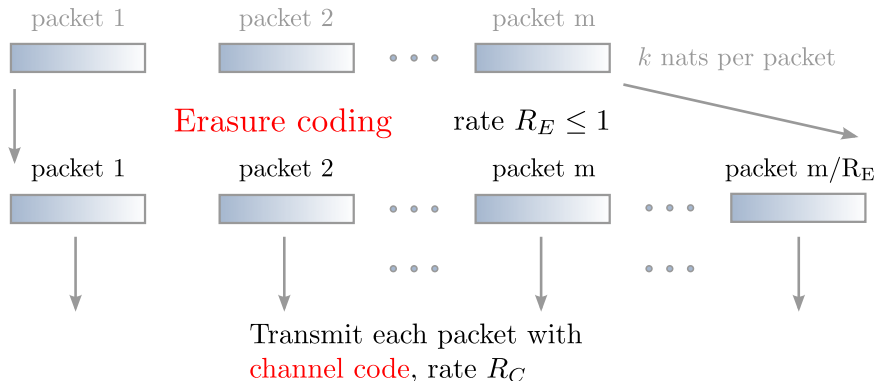


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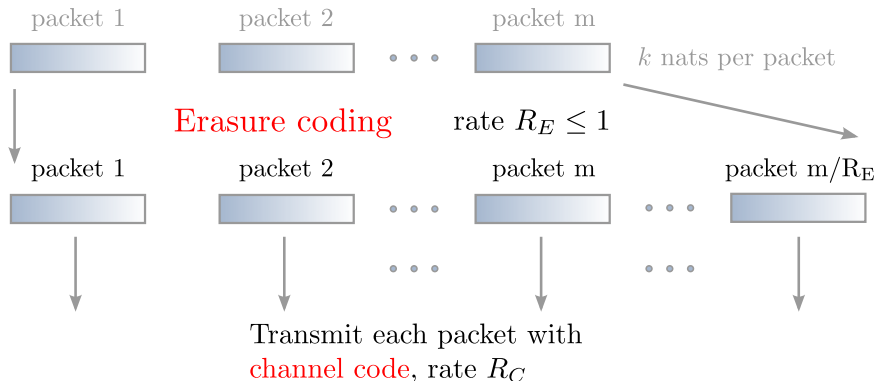
Overall T channel
time units to transmit

Problem Setup



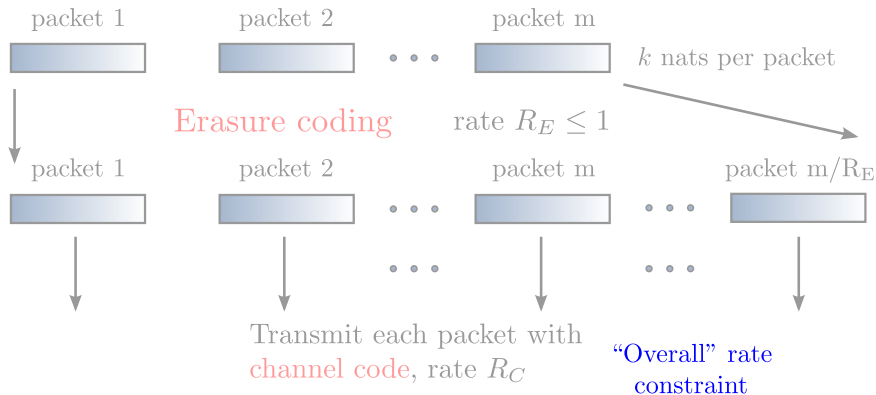
Overall T channel time units to transmit $\implies \frac{k}{R_C}$ channel symbols per packet

Problem Setup



Overall T channel
time units to transmit $\implies \frac{k}{R_C} \cdot \frac{m}{R_E} = T$

Problem Setup



Overall T channel time units to transmit $\implies \frac{k}{R_C} \cdot \frac{m}{R_E} = T \implies \frac{mk}{T} = R_E R_C$

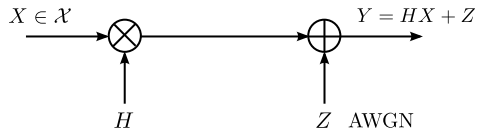
- Fixing a target probability of failure λ and T , what is the minimum operating SNR at PHY

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- For the fixed T , what should be R_C^*, R_E^* ?

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- For the fixed T , what should be R_C^*, R_E^* ?
- How do R_C^*, R_E^* behave as a function of T , as $T \rightarrow \infty$?

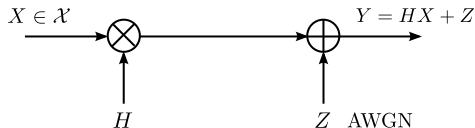
Fading Model

Rayleigh fading channel
Average SNR = P

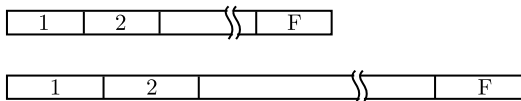


Fading Model

Rayleigh fading channel
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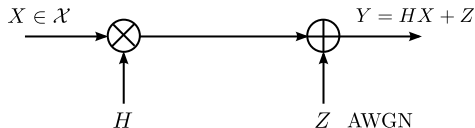
Courtade and Wesel
Block fading



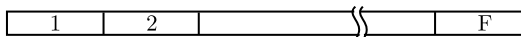
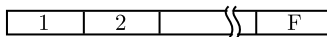
Channel codeword for
two values of R_C

Fading Model

Rayleigh fading channel
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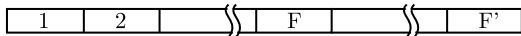
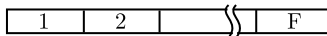


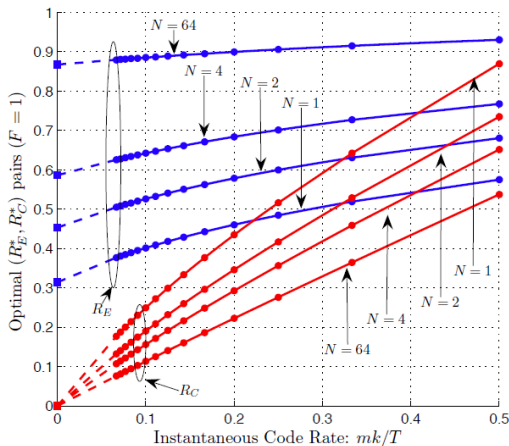
Courtade and Wesel
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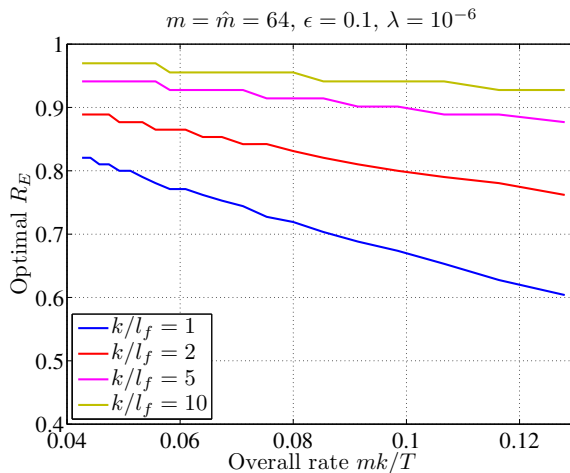
Proportional-diversity
block fading
(PD block fading)





T. A. Courtade and R. D. Wesel, "Optimal allocation of redundancy between packet-level erasure coding and physical-layer channel coding in fading channels," *IEEE Trans. Commun.*, Aug. 2011.

Proportional-Diversity Block Fading



Theorem

Let the coding scheme use an arbitrary erasure code capable of producing any number of packets and a “very good” channel code. Let us assume a Rayleigh proportional-diversity block-fading channel.

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Let the coding scheme use an arbitrary erasure code capable of producing any number of packets and a “very good” channel code. Let us assume a Rayleigh proportional-diversity block-fading channel. Then, for any sufficiently large T the optimal value of R_E is equal to its highest possible value.

- We designed new PBRL codes for short block-lengths

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- We studied a cross-layer rate allocation problem
 - We showed that erasure coding is unnecessary if there is enough diversity at the PHY layer

Acknowledgement

I would like to thank. . .

- Rick & Dariush

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- Engineering Graduate Students Association

Theorem (Smarandache and Vontobel)

Let a protomatrix P with a positive design rate and no punctured variable nodes be of size $n_c \times n_v$. If $S \subseteq [n_v]$, denote by P_S the sub-matrix of P formed by the columns indexed by elements of S . Then, any QC-LDPC code \mathcal{C} obtained from the protomatrix P has a minimum distance $d_{\min}(\mathcal{C})$ that is upper bounded as

$$d_{\min}(\mathcal{C}) \leq \min_{S \subseteq [n_v], |S|=n_c+1}^* \sum_{i \in S} \text{perm}(P_{S \setminus i}), \quad (2)$$

where $|\cdot|$ refers to the cardinality of a set, $S \setminus i$ is shorthand for $S \setminus \{i\}$, and \min^ returns the smallest non-zero value in a set of non-negative values with at least one positive value or $+\infty$ if the set is $\{0\}$.*

Appendix – Permanent Bound Example

$$P = \begin{bmatrix} 2 & 1 & 2 & 1 & 2 & 1 & 0 & 0 & 0 \\ 1 & 2 & 1 & 2 & 1 & 2 & 0 & 0 & 0 \\ 1 & 1 & 1 & 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 & 1 & 1 & 0 & 1 & 0 \\ 1 & 0 & 1 & 0 & 0 & 0 & 0 & 0 & 1 \end{bmatrix}$$

$$S = \{1, 3, 4, 6, 7, 9\}$$

Appendix – Permanent Bound Example

$$P = \begin{bmatrix} 2 & 1 & 2 & 1 & 2 & 1 & 0 & 0 & 0 \\ 1 & 2 & 1 & 2 & 1 & 2 & 0 & 0 & 0 \\ 1 & 1 & 1 & 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 & 1 & 1 & 0 & 1 & 0 \\ 1 & 0 & 1 & 0 & 0 & 0 & 0 & 0 & 1 \end{bmatrix}$$

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$$\sum_{i \in S} \text{perm}(P_{S \setminus i})$$

Appendix – Permanent Bound Example

$$P = \begin{bmatrix} 2 & 1 & 2 & 1 & 2 & 1 & 0 & 0 & 0 \\ 1 & 2 & 1 & 2 & 1 & 2 & 0 & 0 & 0 \\ 1 & 1 & 1 & 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 & 1 & 1 & 0 & 1 & 0 \\ 1 & 0 & 1 & 0 & 0 & 0 & 0 & 0 & 1 \end{bmatrix}$$

$$S = \{1, 3, 4, 6, 7, 9\}$$

$$\sum_{i \in S} \text{perm}(P_{S \setminus i}) = 10$$

Appendix – Permanent Bound Example

$$P = \begin{bmatrix} 2 & 1 & 2 & 1 & 2 & 1 & 0 & 0 & 0 \\ 1 & 2 & 1 & 2 & 1 & 2 & 0 & 0 & 0 \\ 1 & 1 & 1 & 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 & 1 & 1 & 0 & 1 & 0 \\ 1 & 0 & 1 & 0 & 0 & 0 & 0 & 0 & 1 \end{bmatrix}$$

$$S = \{1, 3, 4, 6, 7, 9\}$$

$$\sum_{i \in S} \text{perm}(P_{S \setminus i}) = 10 + 10$$

Appendix – Permanent Bound Example

$$P = \begin{bmatrix} 2 & 1 & 2 & 1 & 2 & 1 & 0 & 0 & 0 \\ 1 & 2 & 1 & 2 & 1 & 2 & 0 & 0 & 0 \\ 1 & 1 & 1 & 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 & 1 & 1 & 0 & 1 & 0 \\ 1 & 0 & 1 & 0 & 0 & 0 & 0 & 0 & 1 \end{bmatrix}$$

$$S = \{1, 3, 4, 6, 7, 9\}$$

$$\sum_{i \in S} \text{perm}(P_{S \setminus i}) = 10 + 10 + 4$$

Appendix – Permanent Bound Example

$$P = \begin{bmatrix} \textcolor{red}{2} & 1 & \textcolor{red}{2} & 1 & 2 & \textcolor{olive}{1} & 0 & 0 & 0 \\ \textcolor{red}{1} & 2 & \textcolor{red}{1} & \textcolor{red}{2} & 1 & \textcolor{olive}{2} & 0 & 0 & 0 \\ \textcolor{red}{1} & 1 & \textcolor{red}{1} & 0 & 0 & \textcolor{olive}{0} & \textcolor{red}{1} & 0 & 0 \\ 0 & 0 & 0 & \textcolor{red}{1} & 1 & \textcolor{olive}{1} & 0 & 1 & 0 \\ \textcolor{red}{1} & 0 & \textcolor{red}{1} & 0 & 0 & \textcolor{olive}{0} & 0 & 0 & \textcolor{red}{1} \end{bmatrix}$$

$$S = \{\textcolor{red}{1}, \textcolor{red}{3}, \textcolor{red}{4}, \textcolor{olive}{6}, \textcolor{red}{7}, \textcolor{red}{9}\}$$

$$\sum_{i \in S} \text{perm}(P_{S \setminus i}) = 10 + 10 + 4 + 4$$

Appendix – Permanent Bound Example

$$P = \begin{bmatrix} \textcolor{red}{2} & 1 & \textcolor{red}{2} & 1 & 2 & 1 & 0 & 0 & 0 \\ \textcolor{red}{1} & 2 & 1 & 2 & 1 & 2 & 0 & 0 & 0 \\ 1 & 1 & 1 & 0 & 0 & 0 & \textcolor{olive}{1} & 0 & 0 \\ 0 & 0 & 0 & 1 & 1 & 1 & 0 & 1 & 0 \\ \textcolor{red}{1} & 0 & 1 & 0 & 0 & 0 & \textcolor{olive}{0} & 0 & \textcolor{red}{1} \end{bmatrix}$$

$$S = \{\textcolor{red}{1}, \textcolor{red}{3}, \textcolor{red}{4}, \textcolor{red}{6}, \textcolor{olive}{7}, \textcolor{red}{9}\}$$

$$\sum_{i \in S} \text{perm}(P_{S \setminus i}) = 10 + 10 + 4 + 4 + 20$$

Appendix – Permanent Bound Example

$$P = \begin{bmatrix} 2 & 1 & 2 & 1 & 2 & 1 & 0 & 0 & 0 \\ 1 & 2 & 1 & 2 & 1 & 2 & 0 & 0 & 0 \\ 1 & 1 & 1 & 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 & 1 & 1 & 0 & 1 & 0 \\ 1 & 0 & 1 & 0 & 0 & 0 & 0 & 0 & 1 \end{bmatrix}$$

$$S = \{1, 3, 4, 6, 7, 9\}$$

$$\sum_{i \in S} \text{perm}(P_{S \setminus i}) = 68$$

$$\min^* (\{68, \dots\})$$

Appendix – Permanent Bound Example

$$P = \begin{bmatrix} 2 & 1 & 2 & 1 & 2 & 1 & 0 & 0 & 0 \\ 1 & 2 & 1 & 2 & 1 & 2 & 0 & 0 & 0 \\ 1 & 1 & 1 & 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 & 1 & 1 & 0 & 1 & 0 \\ 1 & 0 & 1 & 0 & 0 & 0 & 0 & 0 & 1 \end{bmatrix}$$

$$S = \{1, 2, 3, 4, 5, 6\}$$

$$\sum_{i \in S} \text{perm}(P_{S \setminus i}) = 164$$

$$\min^* (\{68, 164, \dots\})$$

Appendix – Permanent Bound Example

$$P = \begin{bmatrix} 2 & 1 & 2 & 1 & 2 & 1 & 0 & 0 & 0 \\ 1 & 2 & 1 & 2 & 1 & 2 & 0 & 0 & 0 \\ 1 & 1 & 1 & 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 1 & 1 & 1 & 0 & 1 & 0 \\ 1 & 0 & 1 & 0 & 0 & 0 & 0 & 0 & 1 \end{bmatrix}$$

$\binom{n_v}{n_c+1}$ sets of size $n_c + 1$

For each set $n_c + 1$ permanents
of size $n_c \times n_c$

$\binom{n_v}{n_c+1}(n_c + 1)$ permanents of size $n_c \times n_c$

- A a square matrix, $\text{perm}(A) = \sum_{\sigma} \prod_j A(\sigma(j), j)$

$$A = \begin{bmatrix} \color{red}{2} & 1 & 0 \\ 1 & \color{red}{2} & 4 \\ 2 & 1 & \color{red}{2} \end{bmatrix} \in \mathbb{Z}^{3 \times 3}$$

$$\text{perm}(A) = 8 +$$

$$A = \begin{bmatrix} \color{red}{2} & 1 & 0 \\ 1 & 2 & \color{red}{4} \\ 2 & \color{red}{1} & 2 \end{bmatrix} \in \mathbb{Z}^{3 \times 3}$$

$$\text{perm}(A) = 8 + 8$$

$$A = \begin{bmatrix} 2 & \color{red}{1} & 0 \\ \color{red}{1} & 2 & 4 \\ 2 & 1 & \color{red}{2} \end{bmatrix} \in \mathbb{Z}^{3 \times 3}$$

$$\text{perm}(A) = 8 + 8 + 2$$

$$A = \begin{bmatrix} 2 & 1 & 0 \\ 1 & 2 & 4 \\ 2 & 1 & 2 \end{bmatrix} \in \mathbb{Z}^{3 \times 3}$$

$$\text{perm}(A) = 8 + 8 + 2 + 0$$

$$A = \begin{bmatrix} 2 & 1 & 0 \\ 1 & 2 & 4 \\ 2 & 1 & 2 \end{bmatrix} \in \mathbb{Z}^{3 \times 3}$$

$$\text{perm}(A) = 8 + 8 + 2 + 0 + 8$$

$$A = \begin{bmatrix} 2 & 1 & 0 \\ 1 & 2 & 4 \\ 2 & 1 & 2 \end{bmatrix} \in \mathbb{Z}^{3 \times 3}$$

$$\text{perm}(A) = 26$$

Best algorithm (Ryser) is of complexity $\Theta(\ell \cdot 2^\ell)$ for matrix of size $\ell \times \ell$