

SUMMER UNDERGRADUATE **RESEARCH PROGRAM** High Rate Tail-Biting List Decoder using a Dual Trellis

Ava Asmani, Brendan Towell, Wenhui Sui¹, Richard Wesel² Communications Systems Laboratory, Department of Electrical and Computer Engineering University of California, Los Angeles ¹Daily Lab Supervisor, ²Principal Investigator



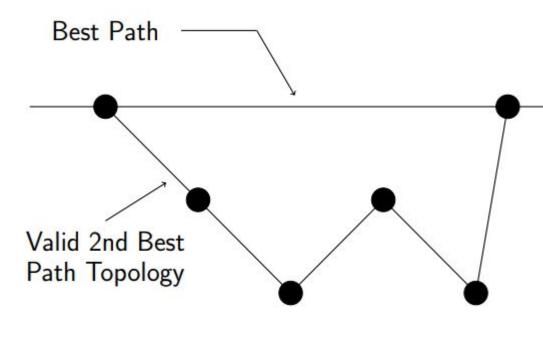
Introduction

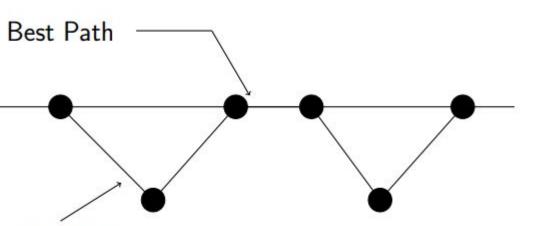
- Transmitting data across noisy channels using high rate convolutional codes and standard maximum likelihood decoding on the encoder-based trellis incurs high decoding costs. To improve efficiency, we implement the dual trellis proposed in Yamada et al. [1], which is based on the parity check matrix of the original code, and improves efficiency for high rate decoding by reducing the number of comparisons performed at each state in the trellis.
- To improve robustness, we built off the work of Liang et al. [5], who demonstrated that for low rate convolutional codes (of the form 1/n), using distance spectrum optimal cyclic redundancy checks (DSO CRCs), in conjunction with serial list decoding, provided significant improvements to signal to noise ratio (SNR) at a fairly low cost.
- Our work extends this approach to high rate codes (of the form (n-1)/n), to identify if such an approach is viable for high rate codes as well.

List Decoding

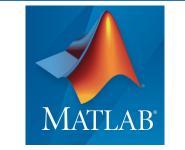
- Adding DSO CRCs provides a further layer of redundancy to rule out invalid decoded messages.
- If a decoded message fails the CRC check, serial list decoding computes the next most likely path by using the paths that were more likely, but failed the CRC.
- The locally second best path will either be one divergence from one of the locally best paths, or a path corresponding to a different starting state, since more than one divergence will always add additional weight compared to one divergence on its own.
- This can be efficiently implemented by using a







Materials



We implemented the dual trellis list Viterbi algorithm with DSO CRCs in C++, and plotted the results in Matlab.



red-black binary search tree, as described by Röder and Hamzaoui [5].

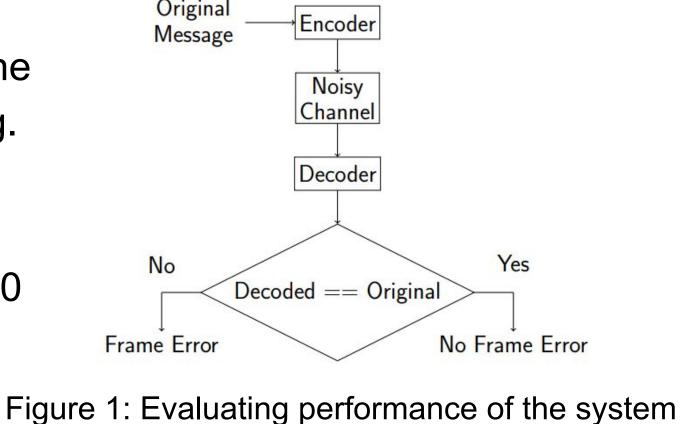
• To cap decoding complexity, we fix the list size, which is the maximum number of messages that can be checked.

Path Topology

Figure 5: Valid and Invalid Next Best Path Topologies

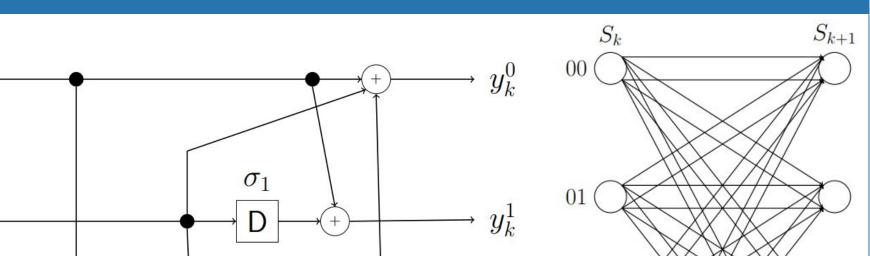
Methods

- We started by building the maximum likelihood high rate tail-biting decoder, then incorporated the dual trellis, and finally implemented list decoding.
- Fig. 1 on the right outlines the process of evaluating the system.
- For each SNR, we ran trials until we reached 200 frame errors, so our FER would have a statistically significant number of errors.



Convolutional Codes

 Convolutional codes use memory elements in the encoding process to add redundancy to the encoded signal, which can be used to $x_k^{\scriptscriptstyle 1}$ correct errors in the received message. • Since the outputs are a function of the memory elements and inputs, this can be viewed as a state machine, which we can arrange on a trellis to account for multiple input blocks. For brevity, only one denoted by sigmas. state transition is shown in Fig. 2.



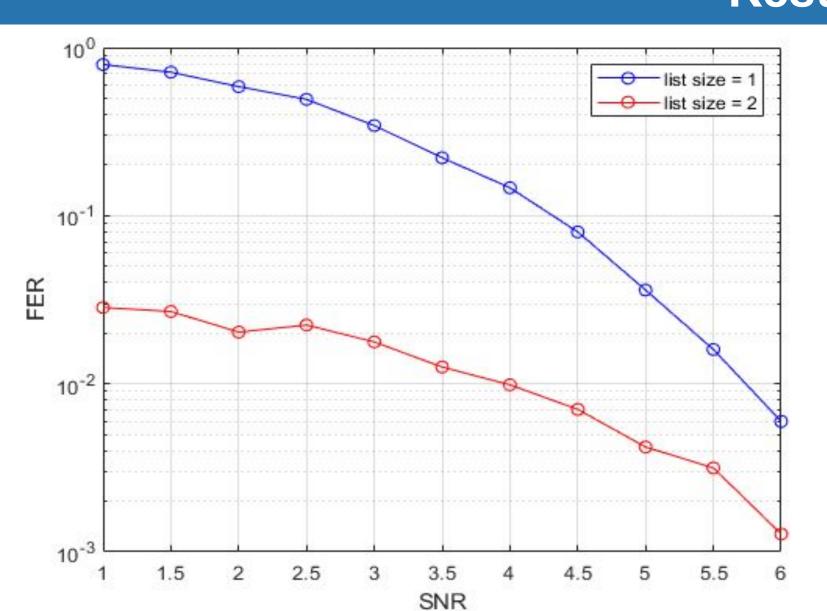


Figure 6: Frame error rate for list sizes of one and two, using the optimal rate-3/4 code with six memory elements, and a blocklength of 30 bits.

Results

- As seen in Fig. 6, increasing list size offers significant improvements in FER for a given SNR.
- Use of the dual trellis reduced decoding complexity by 75% compared to the standard trellis for a rate-3/4 system with two memory elements.
- Larger list sizes produce less frame errors in the decoded message at the cost of increased decoding complexity.

 $y_k^0 y_k^1 y_k^1 y_k^2 y_k^1$ Figure 2: An example of a rate-3/4 convolutional encoder and the corresponding trellis. Each adder is modulo 2, so each value will remain a binary 1 or 0. This encoder has two memory elements,

2

6)

5)

D

Dual Trellis

- For standard maximum likelihood decoding, we want to find the message closest to the received message by comparing all possible paths through the trellis, however, directly enumerating them all is computationally intractable.
- The Viterbi algorithm efficiently finds this sequence by only storing the locally optimal path at each state, (7)upper bounding the number of paths to keep track of at the number of states.

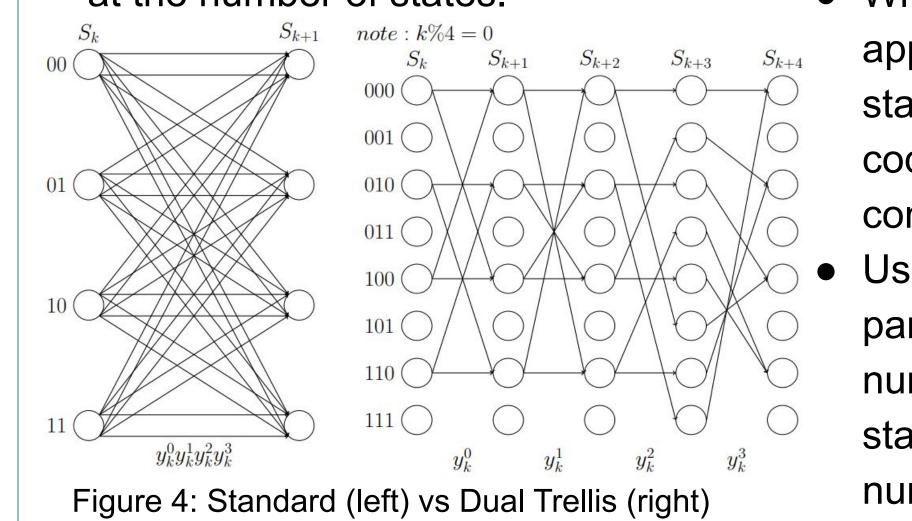


Figure 3: In this small example of one state transition, the Viterbi algorithm would discard the lower paths, since they are locally worse than the top path.

• While highly efficient relative to the naive approach, the Viterbi algorithm on the standard trellis still struggles with high rate

Conclusion, Future Works, and Limitations

- The improvement to FER provided by the list decoding algorithm, with minor increases in decoding complexity, indicates that the method of using S-LVA with DSO CRC for low rate codes presented in Liang et al. [5] generalizes well to high rate codes.
- We're still working on the software implementation, so this poster only includes data for small list sizes. When fully implemented, we plan to test longer list sizes, as well as varying blocklengths and code rates.
- A high rate tail-biting list decoder using a dual trellis accurately and efficiently decodes transmitted data. However, information theory provides theoretical limits on the performance of any communication system, regardless of decoding complexity.

References

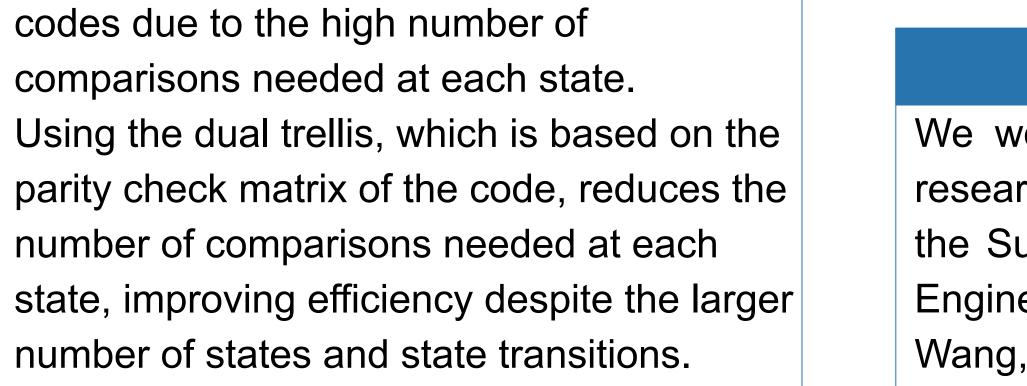
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